TC 2600 Jeff

Organization__________________ Bldg./Room__________

U. S. DEPARTMENT OF COMMERCE
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450
IF UNDELIVERABLE RETURN IN TEN DAYS

OFFICIAL BUSINESS

UNITED STATES POSTAGE
U.S. OFFICIAL MAIL
PENALTY FOR PRIVATE USE $300
PITNEY BOWES
02 1A $ 01.75⁰
0004202245 NOV 09 2004
MAILED FROM ZIP CODE 22202

AN EQUAL OPPORTUNITY EMPLOYER

NOT DELIVERABLE
AS ADDRESSED,
UNABLE TO FORWARD

RECEIVED
DEC -9 2004
TECHNOLOGY CENTER 2800

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/904,355	07/12/2001	Allen He	US018101	1418

7590 11/09/2004

Corporate Patent Counsel
Philips Electronics North America Corporation
580 White Plains Road
Tarrytown, NY 10591

EXAMINER	
FAN, CHIEH M	

ART UNIT	PAPER NUMBER
2634	

DATE MAILED: 11/09/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	09/904,355	HE ET AL.
	Examiner	Art Unit
	Chieh M Fan	2634

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *12 July 2001*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-5* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-5* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☒ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *12 July 2001* is/are: a)☐ accepted or b)☒ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)

2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date *01212003*.

4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .

5) ☐ Notice of Informal Patent Application (PTO-152)

6) ☐ Other: _____.

DETAILED ACTION

Specification

1. The disclosure is objected to. It is not clear how to derive equation 4 on page 6 from equation 3. Following equation 3, it appears that the second part of equation 4 should be "$-(E_s/N)A_0\,[2x(\hat{I}_i - \hat{I}_j) + 2y(\hat{Q}_i - \hat{Q}_j)]$" instead of "$-(E_s/N)A_0\,[2x(\hat{I}_i - \hat{I}_j) - 2y(\hat{Q}_i - \hat{Q}_j)]$". If the examiner is correct, the reminder of the specification and possibly drawings may be to be corrected accordingly. However, the applicants are advised that such correction may introduce new matter into the disclosure, which is not permitted. Further, it appears the variable $\hat{I}_i$ in equation 4 is equal to the variable $\hat{I}_i$ in equation 3 divided (or normalized) by the minimum amplitude A_0. Therefore, the variable $\hat{I}_i$, which is represented by a same symbol, has different meaning in equation 3 and in equation 4. Appropriate correction is required.

Drawings

2. The drawings are objected to because: (a) "$A_0(\hat{I}_i^2 + \hat{Q}_i^2) + A_0(\hat{I}_j^2 + \hat{Q}_j^2)$" in each of the blocks 218 and 224 of Fig. 2 should be "$A_0(\hat{I}_i^2 + \hat{Q}_i^2) - A_0(\hat{I}_j^2 + \hat{Q}_j^2)$", (b) "LLR(m) * A_0/Es/N)" in block 224 of Fig. 2 should be changed to "LLR(m) / ((Es/N)*A_0)", and (c) the output of block 432 in Fig. 4 apparently should be "$-2y(\hat{Q}_i - \hat{Q}_j)$". Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to

ıbandonment of the application. Any amended replacement drawing sheet should ᵎ all of the figures appearing on the immediate prior version of the sheet, even if ıe figure is being amended. The figure or figure number of an amended drawing not be labeled as "amended." If a drawing figure is to be canceled, the ›riate figure must be removed from the replacement sheet, and where necessary, naining figures must be renumbered and appropriate changes made to the brief ›tion of the several views of the drawings for consistency. Additional replacement may be necessary to show the renumbering of the remaining figures. The ement sheet(s) should be labeled "Replacement Sheet" in the page header (as CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the es are not accepted by the examiner, the applicant will be notified and informed of quired corrective action in the next Office action. The objection to the drawings t be held in abeyance.

Claim Objections

Claims 1-5 are objected to because of the following informalities:

Regarding claim 1, "the I component" in line 5 should be changed to --- an I ınent ---; "the Q component" in line 12 should be changed to --- a Q component --

Regarding claim 2, "$2(|\hat{Q}_i - \hat{Q}_j)$" in line 2 should be changed to --- $2(|\hat{Q}_i - \hat{Q}_j)|)$ ---; stored" in line 4 apparently should be removed.

Regarding claim 5, "the I component" in line 5 should be changed to --- an I component ---; "the Q component" in line 12 should be changed to --- a Q component ---.

Appropriate correction is required.

Claim Rejections - 35 USC § 112

4. The following is a quotation of the second paragraph of 35 U.S.C. 112:

> The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

5. Claims 1-5 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Regarding claim 1, the scope of claim cannot be determined. It is not clear to the examiner which parts described in the specification are referred to the claimed "means for determining a first value" (line 4 of claim 1) and "means for determining a second value" (line 11 of claim 1). The present invention is directed to the determination of the scaled log-likelihood ratio, which is equal to $[A_0(\hat{I}_i^2 + \hat{Q}_i^2) - A_0(\hat{I}_j^2 + \hat{Q}_j^2)] - [2x(\hat{I}_i - \hat{I}_j)] + [2y(\hat{Q}_i - \hat{Q}_j)]$ (see equation 5 on page 7 of the specification). Comparing the equation above with the claimed limitations, it is clear that the claimed "a first value" is referred to $-[2x(\hat{I}_i - \hat{I}_j)]$ and the claimed "a second value" is referred to $[2y(\hat{Q}_i - \hat{Q}_j)]$. Therefore, it is not clear which element is referred as the means for determining a first value, particularly when the means determines the first value according to the first value itself.

Similarly, it is not clear which element is referred as the means for determining a second value, particularly when the means determines the second value according to the second value itself. Further, Claim 1 recites the limitation "the minimum amplitude" in lines 16-17. There is insufficient antecedent basis for this limitation in the claim.

Regarding claim 3, claim 3 further limits the means for determining the first value comprises an adder and a sign inverter connected to the adder. It appears such limitations are directed to element 422 and 440 in Fig. 4. As shown in Fig. 4, the sign inverter 422 receives the signal $2x(|\hat{I}_i - \hat{I}_j|)$, and the adder 440 outputs $-([2x(\hat{I}_i - \hat{I}_j)] + [2y(\hat{Q}_i - \hat{Q}_j)])$. Therefore, it is not clear how the claimed means for determining the first value would be consistent with the means for determining a first value recited in the parent claim (claim 1), which requires determining the first value according to the equation $-2x(\hat{I}_i - \hat{I}_j)$. Further, as explained above in claim 1, the first value should be equal to $-2x(\hat{I}_i - \hat{I}_j)$. It is therefore not clear how the output of the adder 440 would be consistent with the claimed limitation "determining a first value".

Regarding claim 4, claim 4 further limits the means for determining the second value comprises an adder and a sign inverter connected to the adder. It appears such limitations are directed to element 432 and 440 in Fig. 4. As shown in Fig. 4, the sign inverter 432 receives the signal $2y(|\hat{Q}_i - \hat{Q}_j|)$, and the adder 440 outputs $-([2x(\hat{I}_i - \hat{I}_j)] + [2y(\hat{Q}_i - \hat{Q}_j)])$. Therefore, it is not clear how the claimed means for determining the first value would be consistent with the means for determining a first value recited in the parent claim (claim 1), which requires determining the second value according to the equation $2y(\hat{Q}_i - \hat{Q}_j)$. Further, as explained above in claim 1, the first value should be

equal to $2y(\hat{Q}_i - \hat{Q}_j)$. It is therefore not clear how the output of the adder 440 would be consistent with the claimed limitation "determining a second value".

Regarding claim 5, the scope of claim cannot be determined. It is not clear to the examiner which parts described in the specification are referred to the claimed "determining a first value" (line 4 of claim 5) and "means for determining a second value" (line 10 of claim 5). The present invention is directed to the determination of the scaled log-likelihood ratio, which is equal to $[A_0(\hat{I}_i^2 + \hat{Q}_i^2) - A_0(\hat{I}_j^2 + \hat{Q}_j^2)] - [2x(\hat{I}_i - \hat{I}_j)] + [2y(\hat{Q}_i - \hat{Q}_j)]$ (see equation 5 on page 7 of the specification). Comparing the equation above with the claimed limitations, it is clear that the claimed "a first value" is referred to $-[2x(\hat{I}_i - \hat{I}_j)]$ and the claimed "a second value" is referred to $[2y(\hat{Q}_i - \hat{Q}_j)]$. Therefore, it is not clear which part of the specification is referred as the step for determining a first value, particularly when the step determines the first value according to the first value itself. Similarly, it is not clear which part of the specification is referred as the step for determining a second value, particularly when the step determines the second value according to the second value itself. Further, Claim 5 recites the limitation "the minimum amplitude" in line 15. There is insufficient antecedent basis for this limitation in the claim.

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Rhee et al. (U.S. Patent No. 6,807,238), Sindhushayana (U.S. Patent No. 6,594,318), Jeong (US 2002/0067777), Tosato et al. (GB 2,388,756).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chieh M Fan whose telephone number is (571) 272-3042. The examiner can normally be reached on Monday-Friday 8:00AM-5:30PM, Alternate Fridays off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Stephen Chin can be reached on (571) 272-3056. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-4700.

Chieh Mi Fan

Chieh M Fan
Primary Examiner
Art Unit 2634

cmf
November 2, 2004

OIPE JC176
JAN 1 7 2003
PATENT & TRADEMARK OFFICE

Substitute for form 1449A/PTO **INFORMATION DISCLOSURE STATEMENT BY APPLICANT** *(use as many sheets as necessary)*				Application Number	09/904,355
				Filing Date	07/12/2001
				First Named Inventor	Allen He
				Group Art Unit	2631
				Examiner Name	Jeffrey Olsen
Sheet	1	of	2	Attorney Docket Number	US01 8101

U.S. PATENT DOCUMENTS

Examiner* Initials	Cite No.[1]	U.S. Patent Document Number	Kind Code[2] (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
						RECEIVED
						JAN 2 1 2003
						Technology Center 2600

FOREIGN PATENT DOCUMENTS

Examiner* Initials	Cite No.[1]	Foreign Patent Document Office[3]	Number[4]	Kind Code (if known)[5]	Name of Patentee or Applicant of Cited Document	Date of Publication MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]
CF		WO	0167617	A	Qualcomm Inc.	09/13/2001		☐
								☐
								☐
								☐
								☐

NON PATENT LITERATURE DOCUMENTS

Examiner* Initials	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[6]
			☐
			☐
			☐
			☐
			☐

Examiner Signature	[signature]	Date Considered	11/2/04

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1] Unique citation designation number. [2] See attached Kinds of U.S. Patent Documents. [3] Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). [4] For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. [5] Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. [6] Applicant is to place a check mark here if English language Translation is attached.

Notice of References Cited	Application/Control No. 09/904,355	Applicant(s)/Patent Under Reexamination HE ET AL.	
	Examiner Chieh M Fan	Art Unit 2634	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,807,238	10-2004	Rhee et al.	375/340
	B	US-6,594,318	07-2003	Sindhushayana, Nagabhushana	375/262
	C	US-2002/0067777	06-2002	Jeong, Gibong	375/324
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	GB 2388756 A	11-2003	United Kingdom	TOSATO et al.	H03M 13/45
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

(12) UK Patent Application (19) GB (11) 2 388 756 (13) A

(43) Date of A Publication 19.11.2003

(21) Application No: 0211492.4

(22) Date of Filing: 17.05.2002

(71) Applicant(s):
Hewlett-Packard Company
(Incorporated in USA - Delaware)
3000 Hanover Street, Palo Alto,
California 94304, United States of America

(72) Inventor(s):
Filippo Tosato
Paola Bisaglia

(74) Agent and/or Address for Service:
Richard Anthony Lawrence
Hewlett-Packard Limited, IP Section,
Filton Road, Stoke Gifford, BRISTOL,
BS34 8QZ, United Kingdom

(51) INT CL[7]:
H03M 13/45 // H04L 25/02 27/26 27/38

(52) UK CL (Edition V):
H4P PAQ PRV

(56) Documents Cited:
US 6078626 A US 5134635 A
US 20020037057 A1

(58) Field of Search:
UK CL (Edition T) H4P PAN PAQ PDT PRV
INT CL[7] H03M 13/41 13/45, H04L 1/00 25/02 27/26 27/34 27/38
Other: Online: EPODOC, JAPIO, WPI, Inspec, Full Text Databases

(54) Abstract Title: **Calculating an estimate of bit reliability in a OFDM receiver by multiplication of the channel state modulus**

(57) A scheme for simplifying the computational complexity of calculating log likelihood ratios (LLR) for soft output demapping is provided. The scheme can be implemented inside a receiver in an OFDM system using multi-level modulation whereby calculation of LLR ratios is accomplished using only multiplication of the channel state (CSI) modulus by d_K (which represents the half distance between partition boundaries in a QAM constellation).

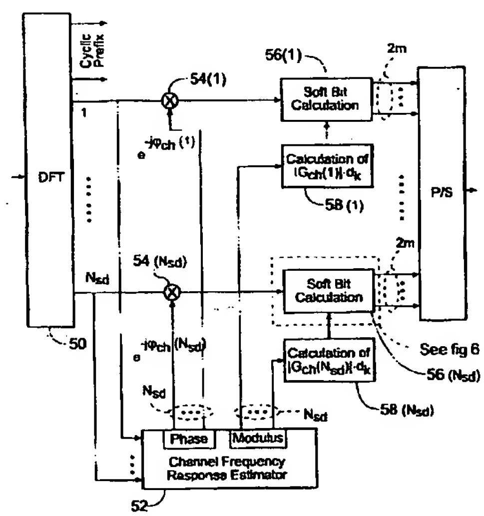


Fig. 5

GB 2 388 756 A

At least one drawing originally filed was informal and the print reproduced here is taken from a later filed formal copy.

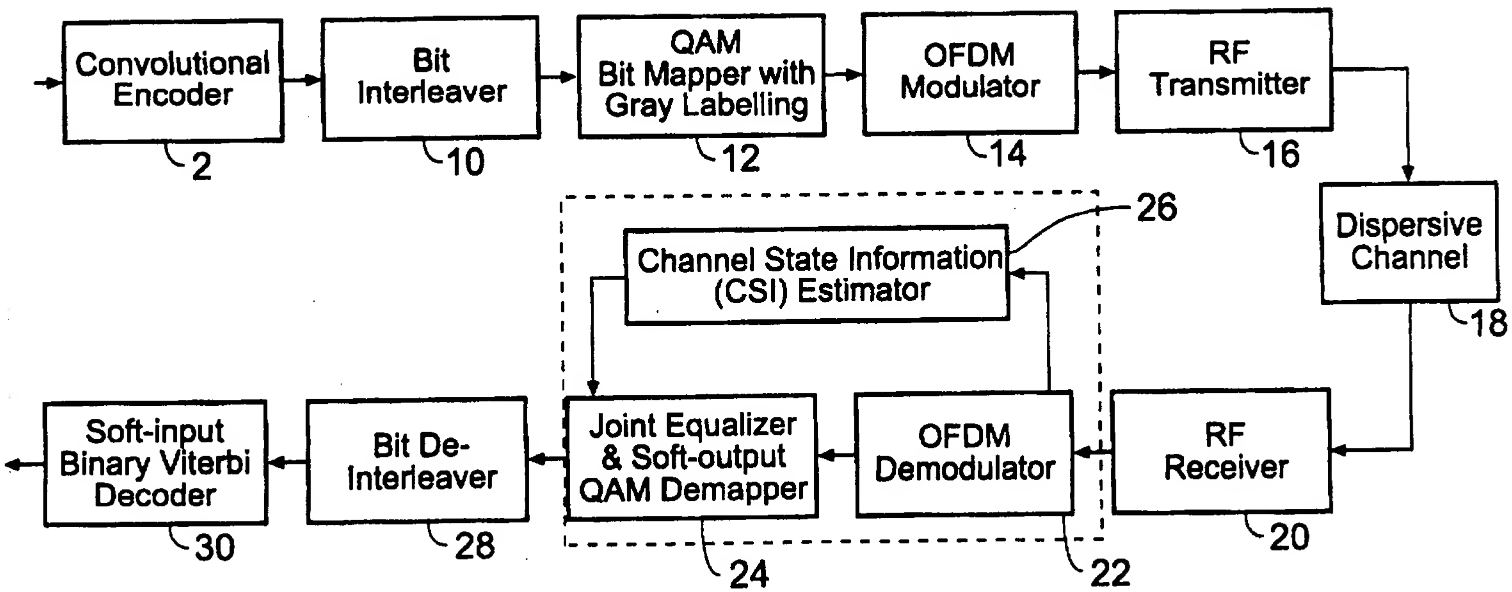

Convolutional Encoder
2
Bit Interleaver
10
QAM Bit Mapper with Gray Labelling
12
OFDM Modulator
14
RF Transmitter
16
Dispersive Channel
18
RF Receiver
20
OFDM Demodulator
22
Channel State Information (CSI) Estimator
26
Joint Equalizer & Soft-output QAM Demapper
24
Bit De-Interleaver
28
Soft-input Binary Viterbi Decoder
30

Fig. 1

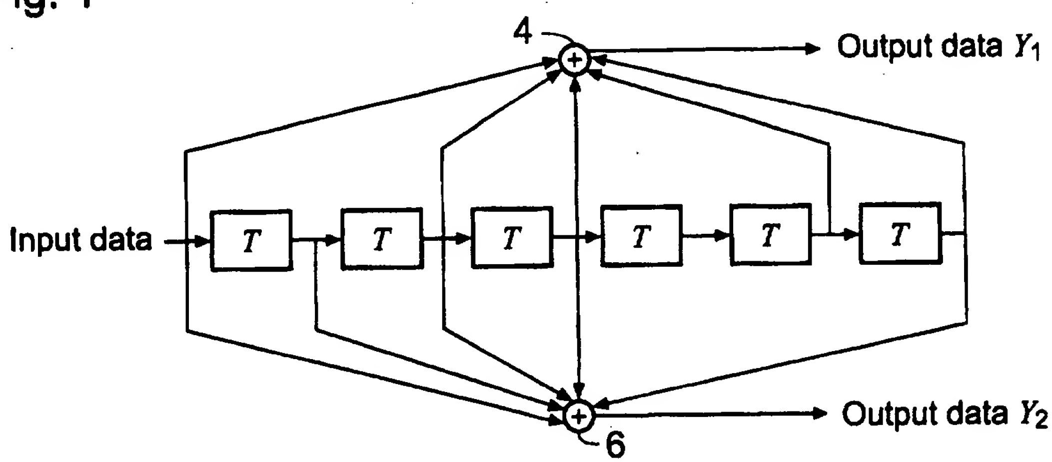

Input data
T
T
T
T
T
T
4
6
Output data Y_1
Output data Y_2

Fig. 2

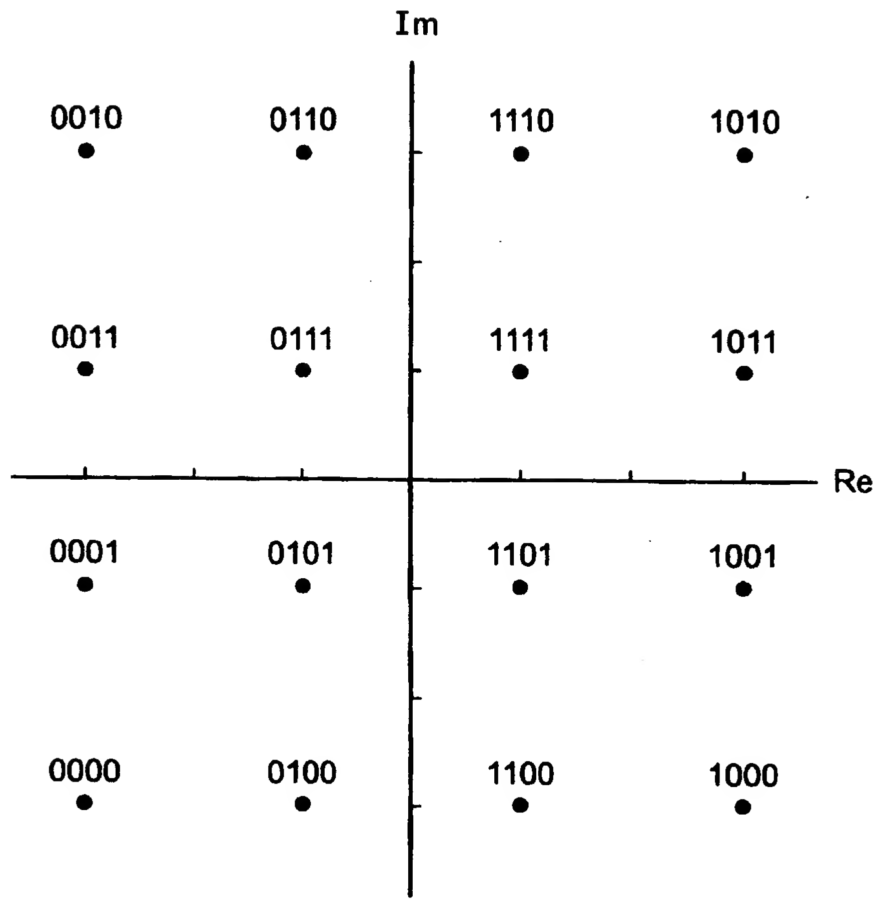

Im
0010
0110
1110
1010
0011
0111
1111
1011
Re
0001
0101
1101
1001
0000
0100
1100
1000

Fig. 3

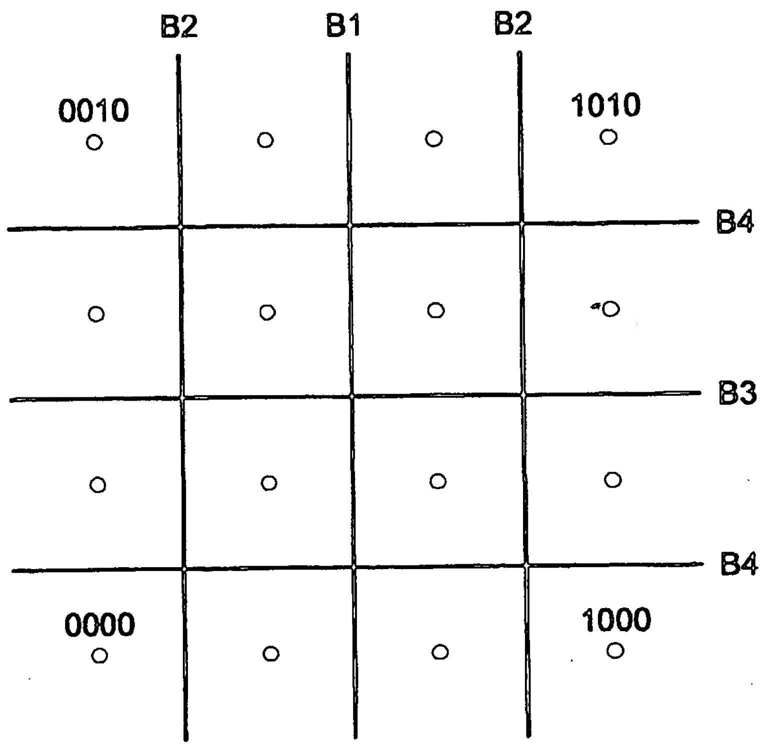


Fig. 4a

24 05 02

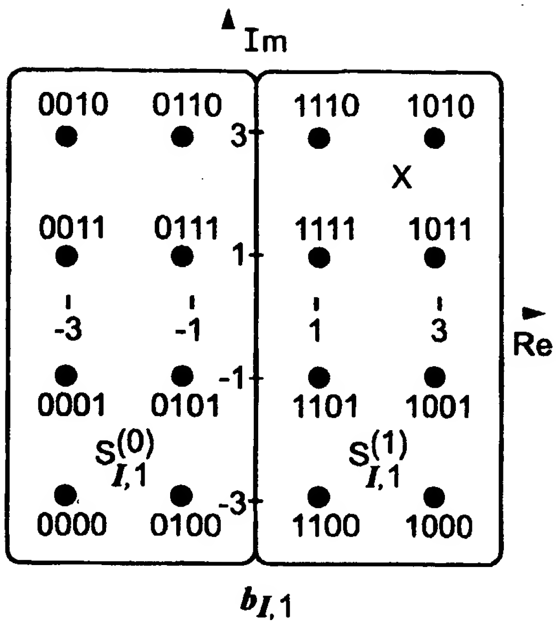


Fig. 4b

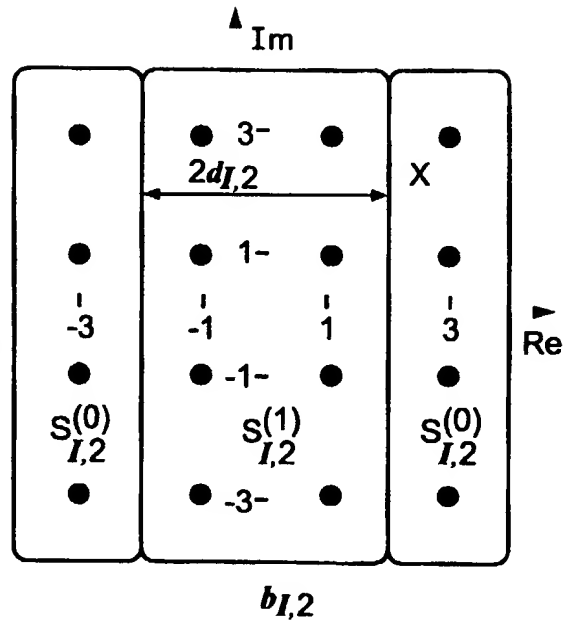


Fig. 4c

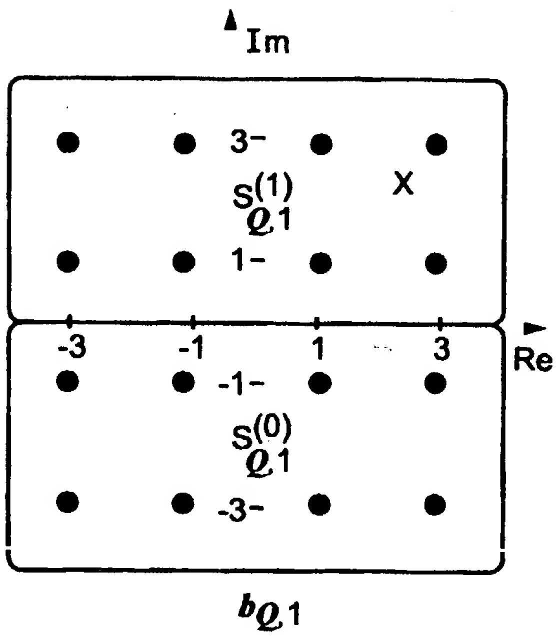


Fig. 4d

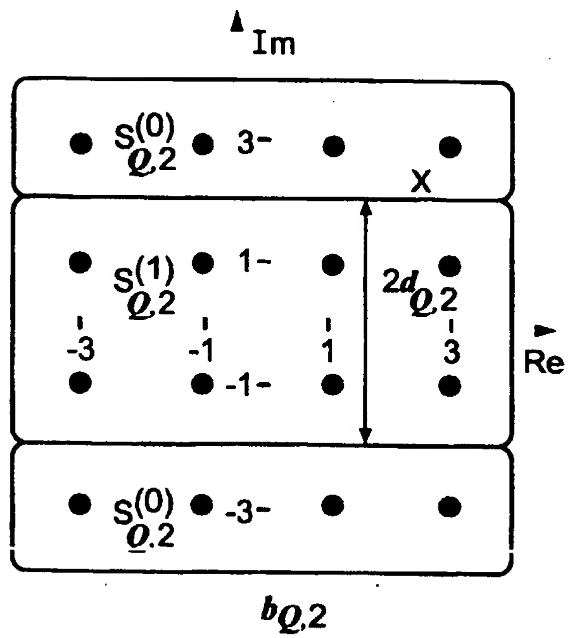
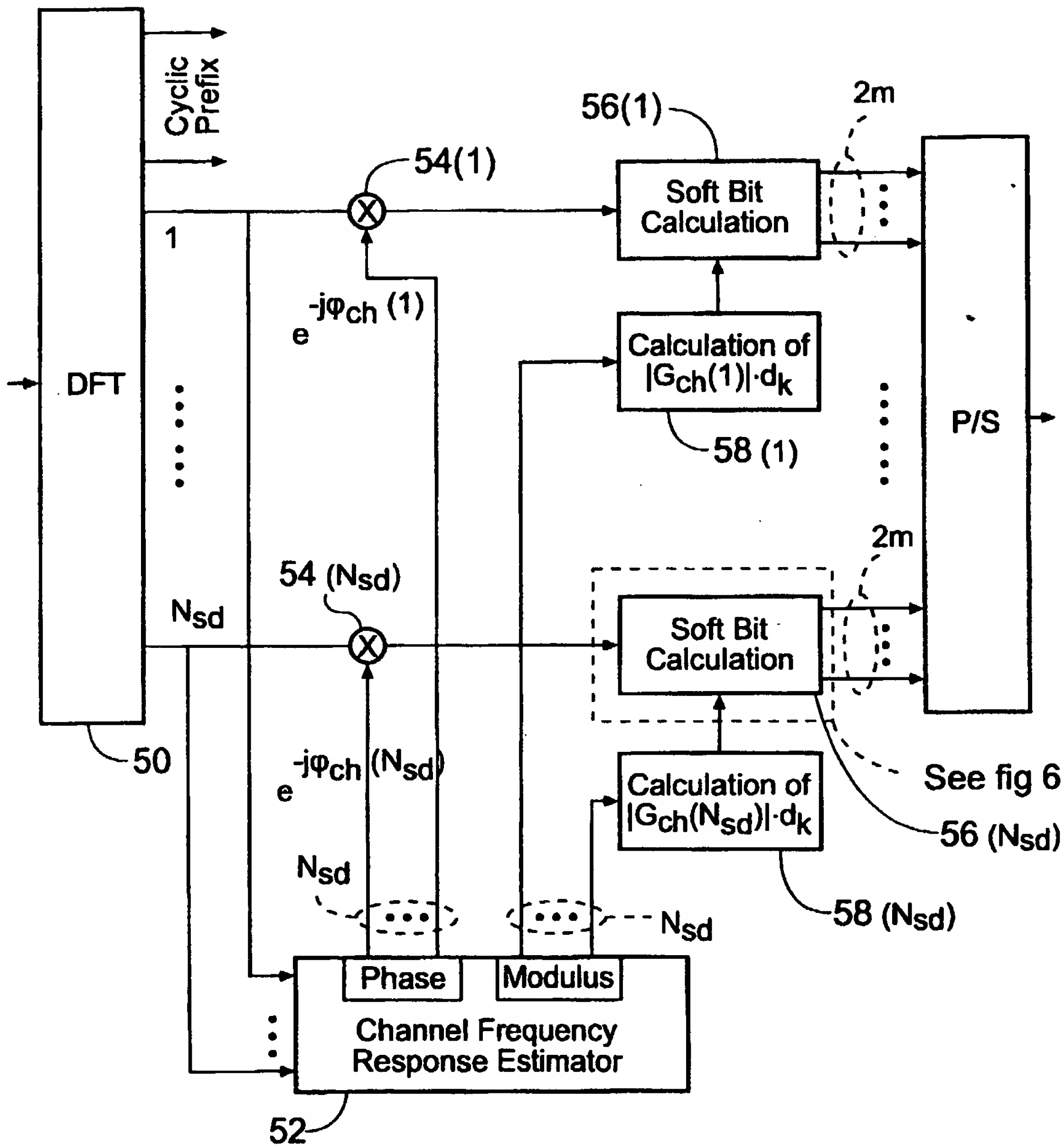


Fig. 4e



DFT
Cyclic Prefix
1
N_{sd}
50
54(1)
54 (N_{sd})
$e^{-j\varphi_{ch}(1)}$
$e^{-j\varphi_{ch}(N_{sd})}$
56(1)
Soft Bit Calculation
Calculation of $|G_{ch}(1)|\cdot d_k$
58 (1)
2m
Soft Bit Calculation
Calculation of $|G_{ch}(N_{sd})|\cdot d_k$
See fig 6
56 (N_{sd})
58 (N_{sd})
2m
P/S
N_{sd}
N_{sd}
Phase
Modulus
Channel Frequency Response Estimator
52

Fig. 5

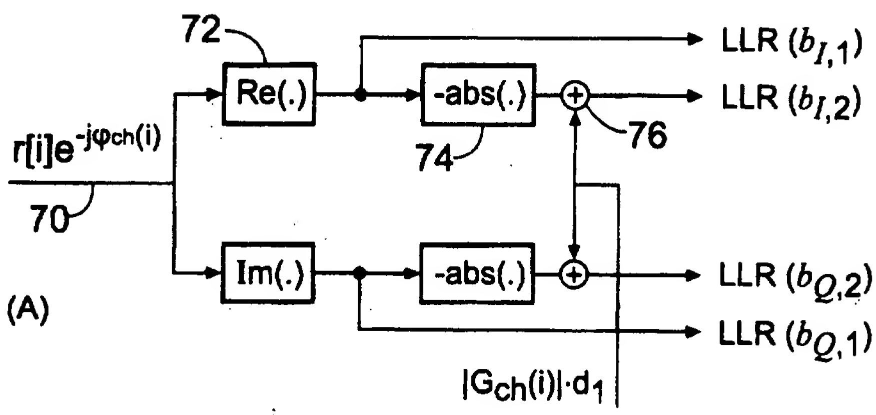

72
Re(.)
-abs(.)
LLR ($b_{I,1}$)
LLR ($b_{I,2}$)
76
74
r[i]e^{-jφch(i)}
70
Im(.)
-abs(.)
(A)
LLR ($b_{Q,2}$)
LLR ($b_{Q,1}$)
$|G_{ch}(i)|\cdot d_1$

Fig. 6

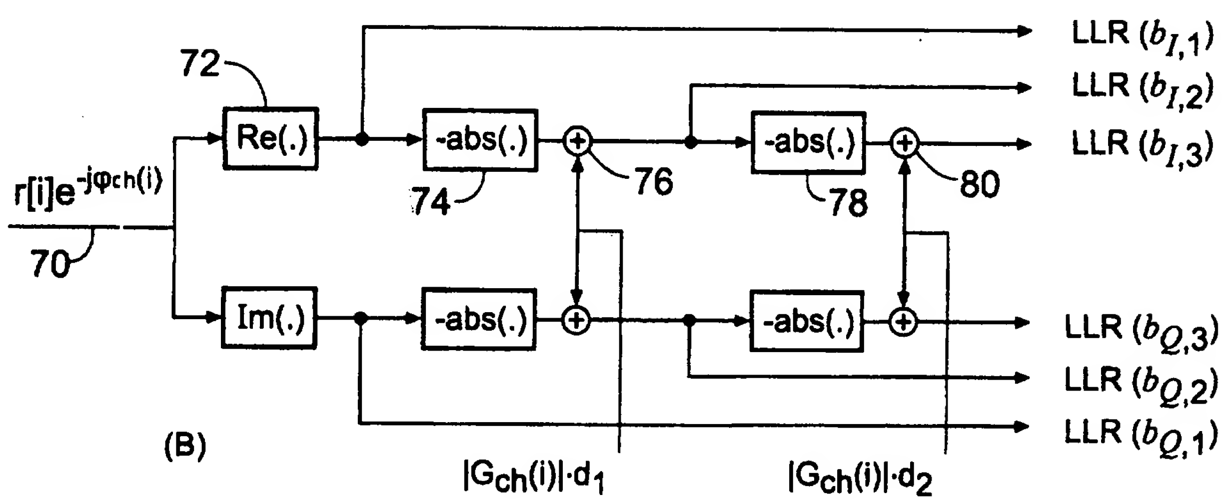

72
Re(.)
-abs(.)
-abs(.)
LLR ($b_{I,1}$)
LLR ($b_{I,2}$)
LLR ($b_{I,3}$)
76
74
78
80
r[i]e^{-jφch(i)}
70
Im(.)
-abs(.)
-abs(.)
(B)
LLR ($b_{Q,3}$)
LLR ($b_{Q,2}$)
LLR ($b_{Q,1}$)
$|G_{ch}(i)|\cdot d_1$
$|G_{ch}(i)|\cdot d_2$

Fig. 7

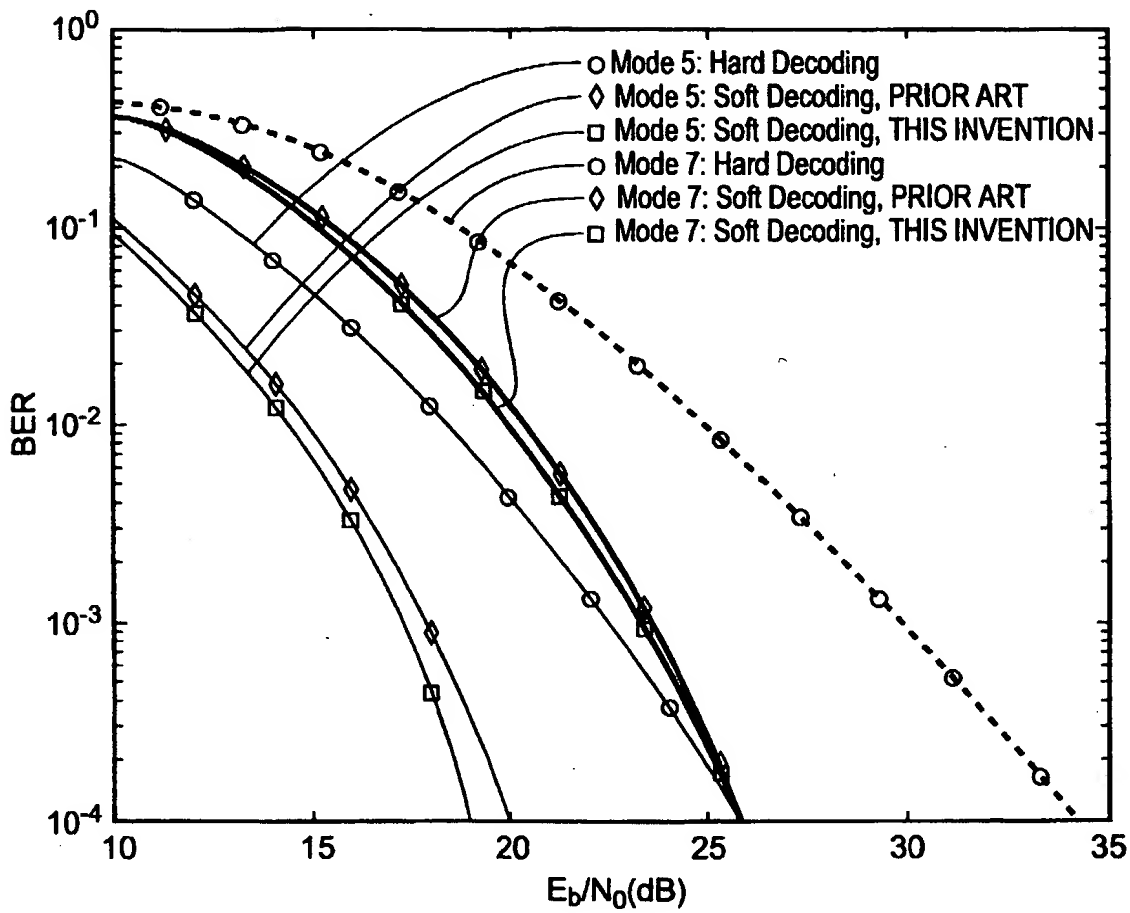

Mode 5: Hard Decoding
Mode 5: Soft Decoding, PRIOR ART
Mode 5: Soft Decoding, THIS INVENTION
Mode 7: Hard Decoding
Mode 7: Soft Decoding, PRIOR ART
Mode 7: Soft Decoding, THIS INVENTION
BER
10^0
10^{-1}
10^{-2}
10^{-3}
10^{-4}
10
15
20
25
30
35
E_b/N_0(dB)

Fig. 8

2388756

A METHOD OF CALCULATING AN ESTIMATE OF RELIABILITY OF BIT INFORMATION IN A OFDM RECEIVER AND A RECEIVER OPERATING IN ACCORDANCE WITH THE METHOD

Background to the Invention

The present invention relates to a method of estimating a reliability measurement for a bit to be decoded in a multi-level transmission scheme based on bit-interleaved coded orthogonal frequency division multiplexing. The method significantly reduces the computational task of the receiver. The present invention also relates to a receiver and demapper operating in accordance with the method.

A significant amount of effort has been expended in the development of high data rate digital communication. Such communication technologies have brought about wireless local area networks, such as those defined in IEEE802.11a and HIPERLAN/2.

Reducing the computational complexity in the receivers of such a network would be beneficial for implementation of such systems because of the limited resources available in many low cost commercial devices.

Figure 1 schematically illustrates a communication system wherein the present invention can be applied. The transmitter part is in accordance with the HIPERLAN/2 and 802.11a standards. In both of these standards different QAM modulation formats are used, such as BPSK, QPSK, 16 QAM and 64 QAM.

In general a first user (who may be human or machine) wishes to transmit a data stream to a second user. The bits in the data stream are firstly convolutionally encoded by an encoder 2 to implement forward error correction. Schemes for convolutional encoding are well known to the person skilled in the art and hence only a brief description is required here. In general convolutional encoding is performed by shifting the data through a chain of delay elements usually implemented as a shift register. In the case of the HIPERLAN/2 and 802.11a standards the convolutional code has a rate ½ and a constraint length of seven. The chain is, as shown in Figure 2 tapped at various points along it and the tapped signals

are summed together at summers 4 and 6 which are implemented as modulo 2 adders, which are often physically realised as a cascade of exclusive - OR gates.

The arrangement shown in Figure 2 has six delay elements T and data can also be tapped off at the input to the first element. The encoder shown has, in effect, seven snapshots of the input string and is therefore described as having a constraint length of 7.

The encoder produces two outputs Y_1 and Y_2 in accordance with the series of connections and taps that are formed within it. Working from the input to the output, the first output is represented by the first, the third, fourth, sixth and seventh taps, or 1011011. The second output Y_2 is represented by 1111001. Thus the code generator for this encoder is [133, 171] in octal format. It should be noted that since the convolutional code has rate ½ the data entering the encoder is a scalar bit stream, whilst the data leaving the encoder is a stream of binary bit pairs. The data output of the encoder 2 is sent to a bit interleaver 10 so as to spread the bits so that potential errors caused by channel degradation are seen as independent at the receiver. The interleaved bits are then converted into quadrature - amplitude modulated (QAM) signals at a QAM mapper 12.

In other words sequences of 2m bits are assigned to one of the $M=2^{2m}$ points in the QAM constellation. The mapping is performed as a Gray coding such that sequences of bits associated with adjacent symbols only differ by one bit.

An exemplary 16-QAM constellation is shown in Figure 3.

The complex (real and imaginary parts) signals are then fed to an orthogonal frequency division multiplexing (OFDM) modulator 14. OFDM is a multi-carrier modulation scheme that subdivides the frequency spectrum that it uses into a plurality of channels. The scheme gives good performance over a dispersive transmission path. The data stream is split into N_{SD} lower rate data streams that are transmitted over N_{SD} sub-carriers. The symbol duration increases for the parallel sub-carriers and hence the transmission scheme is more robust in the presence of multi-path interference.

Finally the signal is transmitted by a radio frequency transmitter 16 such that the radio signal can propagate via a transmission channel 18 to a receiver 20.

The channel may be dispersive and noisy.

Consider the performance of a generic i_{th} sub-carrier used in the OFDM scheme. The i_{th} channel carries a series of QAM symbols. If we consider just one of these, then

$$a[i] = a_I[i] + ja_Q[i] \qquad (1)$$

Where

$a[i]$ represents a symbol in the i_{th} sub-carrier

$a_I[i]$ represents the real component of the symbol

$a_Q[i]$ represents the imaginary component of the symbol.

j represents the imaginary operator.

Each symbol represents a convolutionally encoded bit sequence given by $\{b_{I,1};\ b_{I,2}.....;\ b_{I,m},\ b_{Q,1};\ b_{Q,2};\ ....b_{Q,m}\}$, as shown in Figure 3 for a 16 QAM constellation..

The output of the receiver 20 is provided to an OFDM demodulator 22 whose output is fed to an equalizer and soft-output QAM demapper 24. Assuming that the cyclic prefix, introduced by the OFDM modulator 14, completely eliminates ISI (Inter OFDM Symbol Interference) and ICI (Inter Channel Interference) and that the channel estimate is error free, the received equalized signal in the i_{th} sub-carrier is given by:

$$y[i] = r[i]/G_{ch}(i) = a[i] + w[i]/G_{ch}(i) \qquad (2)$$

Where

$r[i]$ is the received signal before equalization at the output of the OFDM demodulator 22 in the i_{th} sub carrier,

$G_{ch}(i)$ is the Channel Frequency Response (CFR) coefficient (a complex number) in the i_{th} sub-carrier, and

$w[i]$ is the complex Additive White Gaussian Noise (AWGN).

The de-mapper 24 also receives information from a channel state information (CSI) estimator 26. The CSI estimator 26 attempts to deduce the effect of the transmission path on the channel.

In broad terms the channel both scales the QAM signals and rotates them in phase ie the QAM constellation gets rotated. The CSI estimator attempts to estimate the effect of the channel on the symbol. The CSI estimator can work on the assumption that channel is slowly time variant. However, the physical transmission scheme includes "preambles" which contain known sequences of symbols and these can be used to estimate the channel status. Thus individual phase and modulus estimates can be derived for each channel on the basis of identifying the preamble.

The output of the demapper 24 is provided to a de-interleaver 28 and to a convolutional decoder 30. The Viterbi algorithm is a widely used method of decoding convolutional codes. The algorithm searches the possible code words of the convolutional code and detects the one that is most likely to have generated the received sequence. The search procedure steps through the code trellis and for each path along the trellis computes a metric which quantifies the discrepancy between the received sequence and the possible coded sequence. If the information associated to bits fed into the decoder is *hard* (ie binary, a sequence of -1's and 1's) the decoder is called *hard decision* decoder. Alternatively, if the information is *soft*, consisting of a hard decision (the sign) and a confidence level, or reliability (the magnitude), that represents how much confidence there should be in the hard-decision, the decoder is called *soft-decision* decoder. It's well known that, soft decision Viterbi decoding can give significant gain over hard decoding at the expense, however, of a greater computational complexity. In order to implement a soft-decision Viterbi decoder the demapper, which precedes the Viterbi decoder, needs to deliver soft information associated to the bits.

Soft decision demapping for bit interleaved coded modulation (BICM) signals with BPSK or QPSK modulations is straightforward as the soft bit information, before being weighted by the Channel State Information (CSI) is simply given by the received signals for BPSK and by their in-phase and quadrature components for QPSK. Therefore, in the following

discussion consideration will be given to the higher modulation formats, for which soft detection requires much more computational effort.

In the literature, two different approaches can be found to calculate the soft information for BICM signals, with multi-level modulations.

The first prior art mechanism for bit interleaved coded modulation (BICM) schemes was disclosed by E. Zehavi, "8-PSK Trellis Codes for a Rayleigh Channel" IEEE Trans on Comm, Vol 40, pp 873 - 884, May 1992. The process starts by calculating sub-optimal bit metrics that are then used inside a Viterbi decoder for path metric computation.

For each bit $b_{I,K}$ and each bit $b_{Q,K}$ (where I and Q represent in-phase and quadrature parts, respectively, and K represents an index of the bit associated with the symbol where K is an integer in the range $1 \leq K \leq m$) the QAM constellation is split into two partitions of complex symbols.

These partitions are

$S_{I,K}^{(0)}$ having symbols with 0 in position I, K.

$S_{I,K}^{(1)}$ having symbols with 1 in the position I, K.

$S_{Q,K}^{(0)}$ having symbols with 0 in the position Q, K.

$S_{Q,K}^{(1)}$ having symbols with 1 in the position Q, K.

The bit metrics are given by

$$M_C(b_{I,K}) = |G_{CH}(i)|^2 \cdot \min_{a \in S_{I,K}^{(c)}} |y[i]-a|^2 \quad , \qquad c=0,1 \tag{3}$$

Finally the metrics are de-interleaved by a de-interleaver 28 and provided as an input to a Viterbi decoder 30.

The Viterbi decoder works according to a well known algorithm which need not be described in detail here. However various web sites give tutorials in Viterbi decoding, such as http://pweb.netcom.com/~chip.f/viterbi/algrthms2.html.

The convolutional encoder 2 functions as a state machine and the Viterbi decoder is furnished with a state map of the state machine showing which state to state transitions are allowed and which ones are disallowed.

In the second prior art approach the QAM symbols are first demodulated by a soft output de-mapper and passed to a soft-input Viterbi decoder, see M. Speth et al, "Low Complexity Space-Frequency MLSE for Multi-User COFDM", IEEE GLOBECOM '99, pp 2395 - 99, Dec. 1999.

In this approach the process seeks to de-map the received signal into soft bits which have the same sign as that provided by a hard decoder and whose magnitude indicates the reliability of the decision.

The soft bit information assigned to bit $b_{l,K}$ can be shown to be given by the log-likelihood ratio (LLR) of the hard decision on $b_{l,K}$ (see R. Pyndiah et al "Near Optimum Decoding of Product Codes", IEEE GLOBECOM 94, pp 339 - 43, Nov. - Dec., 1994) and can be approximated by

$$LLR(b_{l,K}) = \left|\frac{G_{CH}(i)}{4}\right|^2 \left\{ \min_{a\in S_{l,K}^{(0)}} |y[i]-a|^2 - \min_{a\in S_{l,K}^{(1)}} |y[i]-a|^2 \right\} \tag{4}$$

$$LLR(b_{l,K}) \triangleq |G_{CH}(i)|^2 D_{l,K} \tag{5}$$

We define $S'^{(c)}_{l,k} \triangleq \Re\{S^{(c)}_{l,k}\}$ as the subset containing the real parts of the complex symbols of subset $S^{(c)}_{l,k}$, for $c = 0,1$. It can be shown that equation (4) can be rewritten in a simpler form,

$$LLR(b_{l,k}) = \frac{|G_{ch}(i)|^2}{4} \left\{ \min_{a_l\in S'^{(0)}_{l,k}} (y_l[i]-a_l)^2 - \min_{a_l\in S'^{(1)}_{l,k}} (y_l[i]-a_l)^2 \right\} \tag{6}$$

Where the two minima are now taken over real values instead of complex symbols.

If this is, for convenience, explicitly evaluated for the 16 QAM symbols we have

$$D_{I,1} = \begin{cases} y_I[i] & |y_1[i]| \le 2 \\ 2(y_I[i] - 1 & y_I[i] > 2 \\ 2(y_I[i] + 1) & y_I[i] < 2 \end{cases}$$

$$D_{I,2} = -|y_I[i]| + 2 \tag{7}$$

equivalent expressions hold for the quadrature components with "I" replaced by "Q".

It has been demonstrated in F. Tosato and P. Bisaglia "Simplified Soft-Output Demapper for Binary Interleaved COFDM with Application to HIPERLAN/2", IEEE ICC 2002, April-May, 2002, that using the approximate bit metrics $M_C(b_{I,K})$ in equation 3 for path metric calculation inside the Viterbi algorithm is equivalent to demodulating the signals into soft bit values according to equation 6 and then employing a soft Viterbi algorithm for decoding.

The formula for calculating the log likelihood ratio in equation 6 can be further approximated by calculating $|D_{I,K}|$(or indeed $|D_{Q,K}|$) as the distance of the received equalised signal $y[i]$ from the nearest partition boundary within the partitioned QAM space and assigning $D_{I,K}$ (or $D_{Q,K}$ as appropriate) the sign "+" or "-" according to which partition $y[i]$ falls in. The magnitude (or absolute value) is a measure of distance of the received symbol from the decision boundary.

Figure 4a illustrates the decision boundaries B1 to B4 for 16 QAM modulation and Figures 4b to 4e illustrate the resulting partitions in 16 QAM space. The 16 QAM constellation is used by way of an example. However the present invention can be applied to higher order constellations in a similar way.

Furthermore if we let $d_{I,K}$ and $d_{Q,K}$ denote half the distance between the partition boundaries B2 and B4 relative to bit $b_{I,K}$ and $b_{Q,K}$, respectively, then for the 16 and 64 QAM constellations with Gray mapping used in IEEE802.11a and HIPERLAN/2 it can be shown that

$$D_{I,K} \simeq \begin{cases} y_I[i], & K = 1 \\ -|D_{I,K-1}| + d_{I,K}, & K > 1 \end{cases} \tag{8}$$

In terms of computational complexity the prior art system of decoding the symbols, even with all of the simplifications and approximations invoked, is computationally complex.

To illustrate this consider the case of a burst transmission. In such a burst transmission the channel can be assumed to be time-invariant for the duration of the burst. Thus channel state estimation need only be performed once by the receiver at the beginning of each burst.

If we denote N_b the number of bits coded in the data burst and N_{SD} the number of sub-carriers ($N_b >> N_{SD}$) then LLR calculation using the formulae:

$$LLR(b_{I,K}) = |G_{CH}(i)|^2 D_{I,K}$$

$$LLR(b_{Q,K}) = |G_{CH}(i)|^2 D_{Q,K} \tag{9}$$

requires one real multiplication per coded bit, plus computation of N_{SD} squared modulus of complex values per physical data burst, which is equivalent to $2N_{SD}$ real multiplications.

Thus, the approximate LLR calculation requires $(N_b + 2N_{SD})$ real multiplications.

According to a first aspect of the present invention there is provided a method of estimating a measure of trust of data conveyed by a QAM symbol, wherein the measure of trust is calculated as a linear function of the modulus of a channel state estimation.

It is thus possible to reduce the number of calculations, and in particular multiplications, performed in the soft output de-mapping by using an estimate of likelihood derived as a function of $|G_{CH}(i)|$ rather than $|G_{CH}(i)|^2$.

Preferably the estimate of trust for a bit $b_{I,K}$ for K=1, where K represents a bit index within a complex symbol, is calculated as $\Re\{r[i]e^{-j\varphi_{ch}(i)}\}$, where $\Re$ represents the "real" part of a complex number, $r[i]$ is the received symbol in the i_{th} sub-carrier, and $e^{-j\varphi_{ch}(i)}$ represents the reciprocal of the phase response of an i_{th} transmission channel over which the symbol was transmitted.

Preferably the estimate of trust for a bit $b_{Q,K}$ is calculated as $\Im\{r[i]e^{-j\varphi_{ch}(i)}\}$ where $\Im$ represents the imaginary part of a complex number, for K=1.

Preferably the estimate trust of a bit $b_{I,K}$ is further calculated as $-|LLR(b_{I,K-1})| + (|G_{ch}(i)| \cdot d_{I,K})$ for $K > 1$ where $G_{ch}(i)$ represents the channel frequency response complex coefficient on an i_{th} channel and $d_{I,K}$ denotes a half distance between partition boundaries in QAM space, for $K > 1$, and the estimate of trust of a bit $b_{Q,K}$ is calculated as $-|LLR(b_{Q,K-1})| + (|G_{ch}[i]| \cdot d_{Q,K})$ where $d_{Q,K}$ denotes the half distance between partition boundaries in QAM space, for $K > 1$.

Preferably the estimate of trust is approximate log-likelihood ratio.

According to a second aspect of the present invention, there is provided an apparatus for performing the method according to the first aspect of the present invention.

The present invention will now further be described, by way of example only, with reference to the accompanying figures:

Figure 1 schematically illustrates a transmit path and a receive path for a bit interleaved coded modulation scheme;

Figure 2 schematically illustrates a convolutional encoder;

Figure 3 illustrates a square 16 QAM constellation;

Figure 4a illustrates the partition boundaries that are used to partition the 16 QAM space;

Figures 4b to 4e illustrate the resulting partitions for a 16 QAM space;

Figure 5 is a schematic diagram of a channel state estimator and soft output QAM demapper constituting an embodiment of the present invention;

Figure 6 schematically illustrates a soft decision block constituting an embodiment of the present invention for 16 QAM constellation;

Figure 7 schematically illustrates a soft decision block constituting an embodiment of the present invention for 64 QAM; and

Figure 8 is a comparison of decoding schemes, with the curves showing simulations for performance of a HIPERLAN/2 system.

It should be noted that, for simplicity, the present invention is described with respect to 16 QAM and 64 QAM, but it can be applied to larger constellations and similar demapping schemes can be derived.

Unlike single carrier systems in which all symbols are affected by the same signal to noise ratio (on average), a multi-carrier OFDM system of the type shown in Figure 1 is such that each individual carrier suffers from an individual signal to noise ratio. However it is clear to the person skilled in the art that data conveyed on channels having a high signal to noise ratio is *a priori* more reliable than data transmitted on channels suffering from a low signal to noise ratio. This additional information has, in the prior art, been encoded by weighting the LLR functions by the square modulus of the channel frequency response, which represents the channel state information.

However the inventors have realised that using the modulus of the channel frequency response coefficients instead of the square of the modulus for calculating "soft bit" information for use by a decoder results in only a slight performance loss in terms of bit error rate at a given signal strengths as represented by E_b/N_o, where E_b is the energy per information bit and N_o is the power spectral density of the noise.

This approximation does, however, allow for a computationally efficient implementation of a one tap equaliser and LLR calculation subsystem. Thus, the inventors have realised that sub-optimum soft input Viterbi decoding of a binary interleaved coded OFDM signal can be achieved with little additional complexity compared to realising the same operation using hard decoding instead.

By applying this approximation to the prior art scheme for calculating the LLRs, the following equations are obtained.

$$LLR(b_{I,K}) = \begin{array}{ll} \Re\{r[i]e^{-j\varphi_{ch}(i)}\}, & K=1 \\ -\left|LLR(b_{I,K-1})\right| + |G_{ch}(i)| \cdot d_{I,K} & K \geq 1 \end{array}$$

$$LLR(b_{Q,K}) = \begin{cases} \Im\{r[i]e^{-j\varphi_{ch}(i)}\}, & K=1 \\ -\left|LLR(b_{Q,K-1})\right| + |G_{ch}(i)| \cdot d_{Q,K} & K \geq 1 \end{cases} \qquad (10)$$

Where $\qquad G_{ch}(i) = |G_{ch}(i)|e^{j\varphi_{ch}(i)}$

Similar results hold for other Gray labelling patterns to that shown in Figure 3.

Thus, compared with the prior art calculations of LLR, it can be seen that inside the LLR expression only the thresholds $d_{I,K}$ and $d_{Q,K}$ are scaled by the coefficients that convey the channel state information.

As a consequence, the following scheme can be adopted for joint OFDM signal equalisation and LLR computation. A block diagram for implementing the scheme is illustrated in Figure 5.

The incoming OFDM signal from a receiver is converted into individual data channels 1 to N_{SD} by a Fourier transform block 50. The phase of the signal is then equalised/corrected. This is done by sending each one of the channels to an input of a channel frequency response estimator 52 which estimates the phase of each one of the channels and thereby produces a phase correction signal $e^{-j\varphi_{ch}(i)}$ for each channel i, where i is an integer in the range $1 \leq i \leq N_{SD}$. The channel state estimate is done once per physical burst, at the beginning thereof.

The phase corrections are applied to each of the channels via respective multipliers 54(1) to 54(N_{SD}). The phase equalised channel signals are then supplied to first inputs of respective soft bit calculators 56(1) to 56(N_{SD}).

The channel frequency response estimator 52 also estimates the modulus of the signal strength in each one of the channels, and this information is supplied to a threshold calculator 58(1) to 58(N_{SD}) associated with each individual channel which calculates the threshold values $|G_{ch}(i)| \cdot d_{I,K}$ and $|G_{ch}(i)| \cdot d_{Q,K}$ where i represents the channel number. These values are then passed to the respective soft bit calculators 56.

The specific implementation of the soft bit calculators is shown in Figure 6, for a 16 QAM constellation and in Figure 7 for a 64 QAM constellation.

For the arrangement shown in Figure 6, the received phase equalised signal is received at an input 70 and supplied to a first analyser 72 which calculates the real component of the signal. The output of the first analyser represents bit value $LLR(b_{I,1})$. The value $LLR(b_{I,1})$is further passed through an absolute value former 74 which calculates and negates the absolute value of $LLR(b_{I,1})$. The output of the absolute value former 74 is then added to $|G_{ch}(i)| \cdot d_I$ by an adder 76 to yield an output $LLR(b_{I,2})$. A similar process is implemented on the imaginary component of the input signal to yield $LLR(b_{Q,1})$and $LLR(b_{Q,2})$.

The arrangement shown in Figure 7 is similar to that shown in Figure 6 and like parts are denoted by like reference numerals. The additional features are that the output $LLR(b_{I,2})$ is made available to a further adder absolute value former and negator 78 whose output is then added to $|G_{ch}(i)| \cdot d_2$ by a further 80 to yield $LLR(b_{I,3})$. Similar components are provided in the "imaginary" signal path for calculating $LLR(b_{I,3})$.

Comparing the complexity of this scheme in a burst mode with the prior art, it is now necessary to calculate N_{SD} real multiplications per transmission burst in the case of 16 QAM and $2N_{SD}$ for 64 QAM because only the thresholds d_K need to be scaled by the cannel state information coefficients.

However, these multiplications are not actual multiplications because for square QAM constellations d_K are powers of two, so in fact only bit-wise shifts are needed for threshold scaling.

It is, however, necessary to calculate N_{SD} moduli of the complex values per physical burst to calculate the channel state information values. This requires $2N_{SD}$ real multiplications and N_{SD} square roots to be formed.

Comparing the computational workload:

Present invention = $2N_{SD}$ real multiplications $+N_{SD}$ square roots.

Prior art = $2N_{SD}$ real multiplications $+N_b$ real multiplications.

The computational advantage comes because $N_b >> N_{SD}$ and in practice N_{SD} is 48 in HIPERLAN/2 whereas N_b is likely to be several thousand.

The performance of the present invention compared to the computationally complex prior art is shown in Figure 8. For a given bit error rate the degration in sensitivity is only a fraction of a dB for 16 QAM and is negligible for 64 QAM at BER 10^{-4}. The approximation gets tighter for larger constellation size, where more calculations are saved by the approximate demapping. The graph compares hard decoding, prior art soft decoding and soft decoding according to the present invention in modes 5 and 7 of HIPERLAN/2 using 16 and 64 QAM respectively.

CLAIMS

1. A method of estimating a measure of trust of data conveyed by a QAM symbol, wherein the measure of trust is calculated as a linear function of the modulus a channel state estimation.

2. A method as claimed in claim 1, wherein the measure of trust is a log-likelihood ratio.

3. A method as claimed in claim 2 where the $LLR(b_{I,K})$, for K=1, is calculated as $\Re\{r[i]e^{-j\varphi_{ch}(i)}\}$, where $\Re$ represents the "real" part of a complex number, $r[i]$ is the received symbol in the i_{ch} channel, and $e^{-j\varphi_{ch}(i)}$ represents the reciprocal of the phase response of the i_{th} transmission channel over which the symbol was transmitted, where K represents a bit index within a complex symbol.

4. A method as claimed in claim 3, where the $LLR(b_{Q,K})$ for K=1 is calculated as $\Im\{r[i]e^{-j\varphi_{ch}(i)}\}$ where $\Im$ represents the imaginary part of a complex number.

5. A method as claimed in claim 3, where the $LLR(b_{I,K})$ is calculated as $-|LLR(b_{I,K-1})|+(|G_{ch}(i)|\cdot d_{I,K})$ for $K>1$ where $G_{ch}(i)$ represents the channel frequency response complex coefficient on an i_{th} channel and $d_{I,K}$ denotes a half distance between partition boundaries in QAM constellation.

6. A method as claimed in claim 3, where the $LLR(b_{Q,K})$ is calculated as $-|LLR(b_{Q,K-1})|+(|G_{ch}[i]|\cdot d_{Q,K})$ for $K>1$ where $d_{Q,K}$ denotes the half distance between partition boundaries in QAM constellation.

7. A method as claimed in claim 3, wherein thresholds $d_{I,K}$ are scaled by the amplitude of the channel state estimator coefficients $G_{ch}(i)$.

8. A method as claimed in claim 7, in which computational complexity is reduced by using the modulus of the estimated channel state estimator coefficients $|G_{ch}(i)|$ instead of $|G_{ch}(i)|^2$ as channel state information inside a soft output demapper.

9. A method as claimed in claim 1, wherein the QAM symbol is a symbol in a OFDM transmission scheme using multi-level modulation.

10. A soft output demapper operating in accordance with the method claimed in claim 1.

pplication No: GB 0211492.4
laims searched: 1-10

Examiner: Owen Wheeler
Date of search: 27 November 2002

'atents Act 1977
;earch Report under Section 17

Databases searched:

JK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK Cl (Ed.T): H4P (PAN, PAQ, PDT, PRV)

Int Cl (Ed.7): H03M: 13/41, 13/45;
H04L: 1/00, 25/02, 27/26, 27/34, 27/38

Other: Online: EPODOC, JAPIO, WPI, Inspec, Full Text Databases

Documents considered to be relevant:

Category	Identity of document and relevant passage		Relevant to claims
X	US 2002037057 A1	[KROEGER] See para 14	1 at least
X	US 6078626 A	[RAMESH] See equations in columns 5 and 6	1 at least
X	US 5134635 A	[HONG] See fig 2a and columns 3 and 4	1 at least

X	Document indicating lack of novelty or inventive step	A	Document indicating technological background and/or state of the art.
Y	Document indicating lack of inventive step if combined with one or more other documents of same category	P	Document published on or after the declared priority date but before the filing date of this invention
		E	Patent document published on or after, but with priority date earlier than, the filing date of this application
&	Member of the same patent family		